

Electronic Acknowledgement Receipt

EFS ID:	1374576
Application Number:	10517181
International Application Number:	
Confirmation Number:	5687
Title of Invention:	Phase locked loop circuit, electronic device including a phase locked loop circuit and method for generating a periodic signal
First Named Inventor/Applicant Name:	Johannes Wilhelmus Theodorus Eikenbroek
Customer Number:	27045
Filer:	John Han/Pamela J Shultz
Filer Authorized By:	John Han
Attorney Docket Number:	P15069-US1
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	Transmittal-of-IDS-for-Filing-P15069-US1.pdf	131725	no	3

Warnings:

Information:					
This is not an USPTO supplied IDS fillable form					
2	NPL Documents	BEARDS-an-oversampling-delta-sigma-frequency-discriminator.pdf	2223219	no	7
Warnings:					
Information:					
3	Foreign Reference	WO_1995031861_A1.pdf	1262052	no	30
Warnings:					
Information:					
Total Files Size (in bytes):			3616996		
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